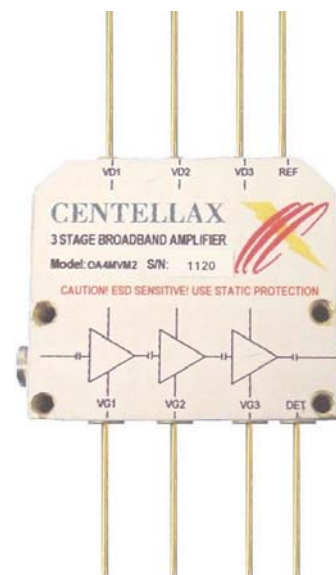


OA4MVM2 40 Gb/s Broadband Driver Amplifier

Features

- 7.5 Vp-p (23 dBm saturated output power)
- < 8 ps rise/fall time
- <0.5 ps added rms jitter
- 27 dB gain (to 45 GHz)
- 3.4 W Power Dissipation
- Useful gain to 65 GHz
- Small Size Package



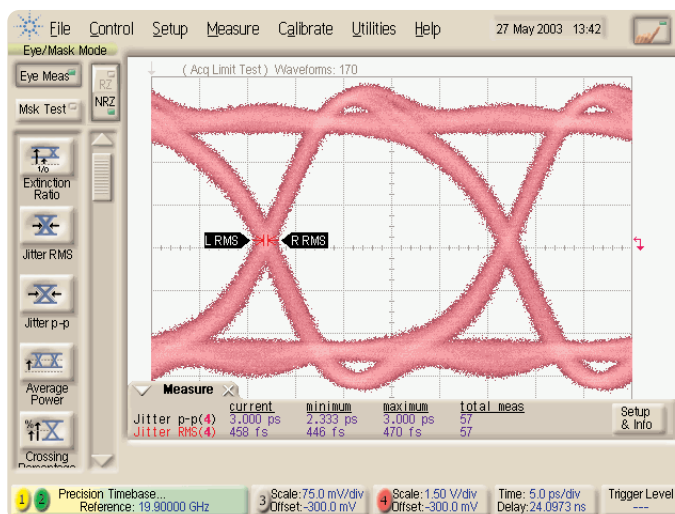
Description

The OA4MVM2 Driver Amplifier is a general-purpose broadband amplifier designed especially for SONET OC-768 Mach-Zehnder optical modulator driver applications. Its exceptional performance and small size make it an easy addition to your Intermediate Reach, Long Haul, or Ultra Long Haul network infrastructure design.

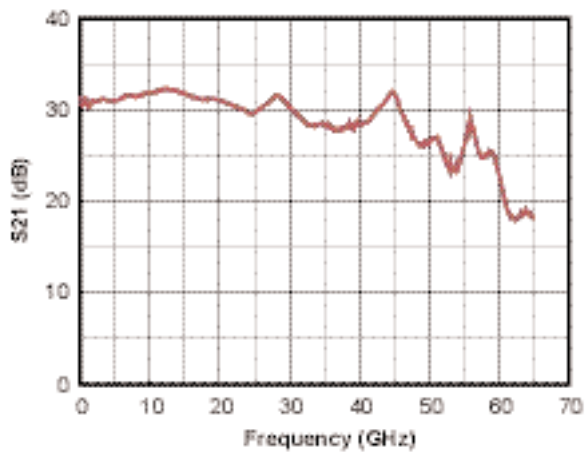
The OA4MVM2 provides a complete driver module package with a wide frequency range of 40 kHz to 65 GHz, low power dissipation, ample drive signal, very low added-jitter, fast rise time, and external control.

Applications

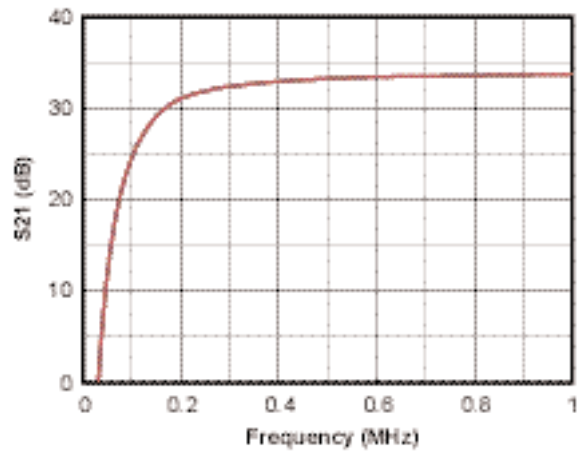
- SONET OC-768/SDH-256 equipment
- Mach-Zehnder optical modulator driver
- High frequency/optical communications test instrumentation
- General purpose gain block



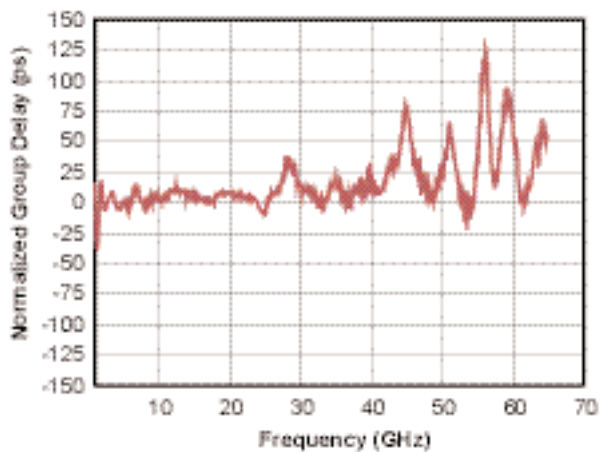
OA4MVM2 ($2^{31} - 1$) EYE: <500 fs added rms jitter (5 ps/div)



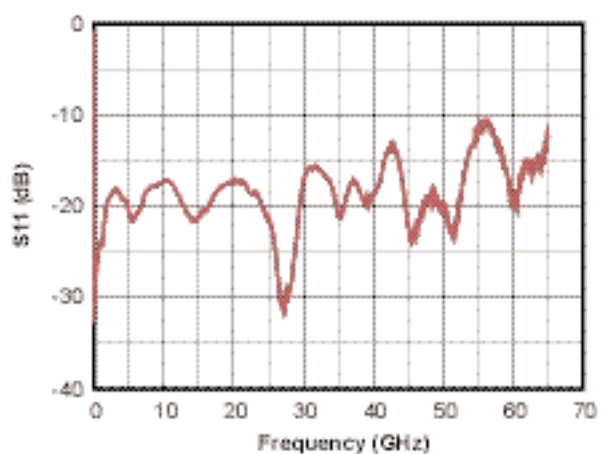
OA4MVM2 typical measured performance
Bias: Vd1 = 7V, Vd2 = 7V, Vd3 = 7V,
Vg1/Vg2/Vg3 = -0.05 V, Vb1/Vb2/Vb3 = float



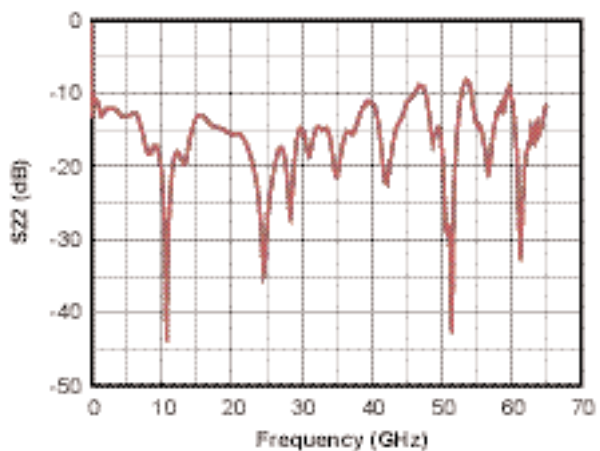
Low frequency S21 measurement



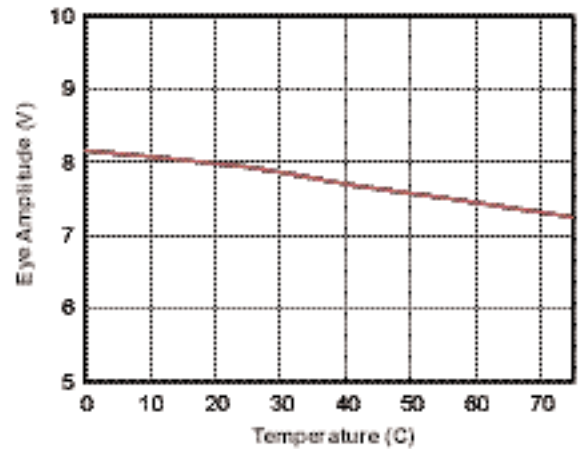
OA4MVM2 typical measured performance
Bias: Vd1 = 7V, Vd2 = 7V, Vd3 = 7V,
Vg1/Vg2/Vg3 = -0.05 V, Vb1/Vb2/Vb3 = float



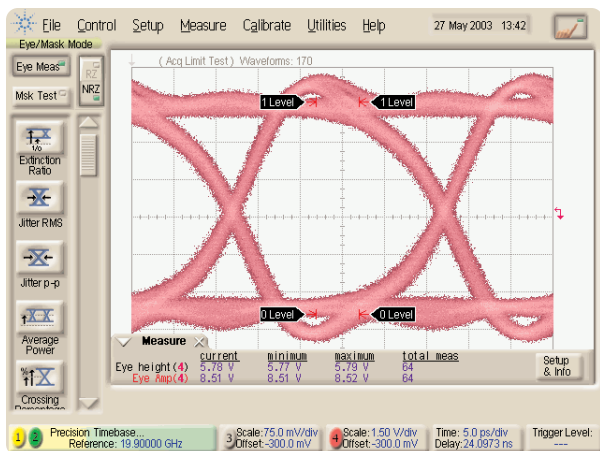
OA4MVM2 typical measured performance
Bias: Vd1 = 7V, Vd2 = 7V, Vd3 = 7V,
Vg1/Vg2/Vg3 = -0.05 V, Vb1/Vb2/Vb3 = float



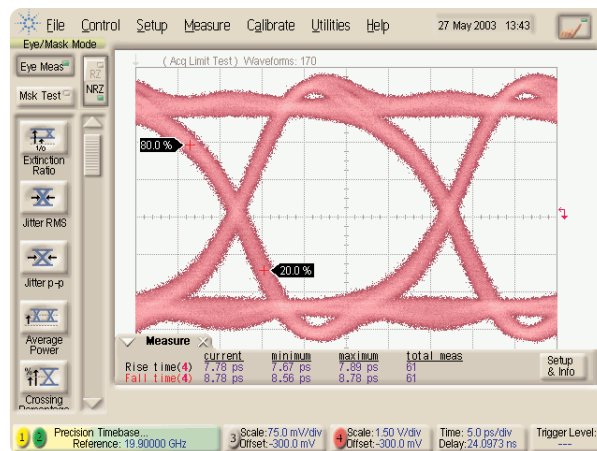
OA4MVM2 typical measured performance
Bias: Vd1 = 7V, Vd2 = 7V, Vd3 = 7V,
Vg1/Vg2/Vg3 = -0.05 V, Vb1/Vb2/Vb3 = float



EYE amplitude vs. temperature



OA4MVM2 ($2^{31} - 1$) EYE: > 7V output (1.5V/div)



OA4MVM2 ($2^{31} - 1$) EYE: < 10 ps Tr/Tf (5 ps/div)

Electrical Specifications @ 25°C

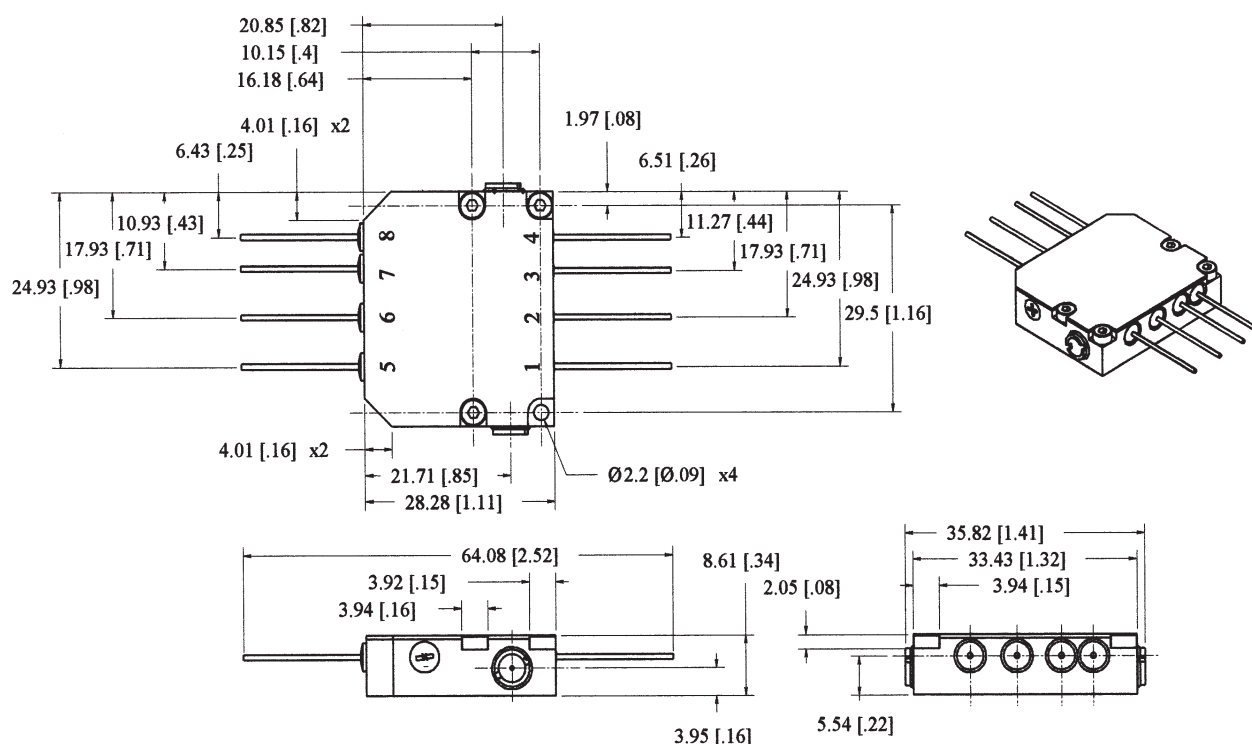
Parameter	Description	Minimum	Typical	Maximum
S21 (dB)	Small Signal Gain			
	0.01 – 26 GHz	27	30	—
	26 – 45 GHz	24	27	—
V_{out} (V)	Output Eye Amplitude	7	7.5	—
V_{in} (V)	Input Eye Amplitude	—	0.5	—
Jitter rms (ps)	Added jitter (rms method)*	—	< 0.5	< 1
Tr/Tf (ps)	Rise/Fall Time	—	< 8	< 10
S11 (dB)	Input Match			
	0.01 – 26 GHz	—	–12	–10
	26 – 45 GHz	—	–10	–8
S22 (dB)	Output Match			
	0.01 – 26 GHz	—	–12	–10
	26 – 45 GHz	—	–10	—
P_{sat} (dBm)	Saturated Power Output	—	23	—

Operating Specifications

Parameter	Description	Minimum	Typical	Maximum
Vdd1 (V)	First Drain Voltage	—	7	8
Vdd2 (V)	Second Drain Voltage	—	7	8
Vdd3 (V)	Third Drain Voltage	—	7	8
Id1 (mA)	First Drain Current	—	85	—
Id2 (mA)	Second Drain Current	—	150	—
Id3 (mA)	Third Drain Current	—	240	—
Vg1 (V)	First Gate Voltage	–0.5	–0.2 to 0	+0.5
Vg2 (V)	Second Gate Voltage	–0.5	–0.2 to 0	+0.5
Vg3 (V)	Third Gate Voltage	–0.5	–0.2 to 0	+0.5
Pdc (W)	Power Dissipation	—	3.4	—
Tbs (°C)	Case Temperature	—	—	75**

$$* (\text{Jitter}_{added})^2 = (\text{Jitter}_{output})^2 - (\text{Jitter}_{input})^2$$

** Four threaded holes are provided for convenient heatsink attachment. The package body temperature must not exceed Tbs maximum.



Physical Characteristics (all measurements in mm [inches])

OA4MVM2 Pin Definition

Pin	Function	Operational Notes
RFin	RF Input	GPPO (m)
RFout	RF Output	GPPO (m)
1 (Vg1)	1st stage gate bias	Set at typical operating specification.
2 (Vg2)	2nd stage gate bias	Set at typical operating specification, adjust for desired EYE
3 (Vg3)	3rd stage gate bias	Set at typical operating specification, adjust for desired EYE
4 (Det)	RF Power Detector	(option)
5 (Vd1)	1st stage drain bias	Set at typical operating specification
6 (Vd2)	2nd stage drain bias	Set at typical operating specification, adjust for desired amplitude
7 (Vd3)	3rd stage drain bias	Set at typical operating specification, adjust for desired amplitude
8 (Ref)	RF Power Reference	(option)

Bias Recommendations (in order):

1) Bias gates; 2) Bias Drains; 3) Adjust for EYE amplitude and cross-over